

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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22 Mar 02
R. Tallo

In re application of:
DAVID R. HEMBREE
ALAN G. WOOD

Division of Serial No. 09/824,152
filed on April 2, 2001

EXAMINER: STEVENSON, A.

Filing Date: August 20, 2001

ART UNIT: 2812

Title: METHOD FOR FABRICATING
SEMICONDUCTOR COMPONENTS

Docket No. 00-0625.1

JC979 U.S. PTO
09/933492
08/20/01

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56
August 20, 2001

Assistant Commissioner of Patents
BOX PATENT APPLICATION
Washington, DC 20231

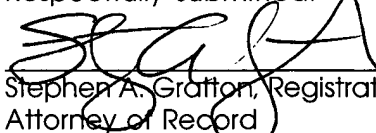
Sir:

The citations listed on the accompanying Form PTO/SB/08, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional material to the examination of this application does not exist, or that any one or more of these citations constitutes prior art under 35 USC §102.

It is requested that the above citations be made of record, if appropriate, in the prosecution of this application.

Respectfully submitted:


Stephen A. Gratton, Registration No. 28,418
Attorney of Record

2764 South Braun Way
Lakewood, CO 80228
Telephone: 303 989 6353

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Date of Deposit: August 20, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Address" Service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner of Patents, BOX PATENT APPLICATION, Washington, D.C. 20231.

August 20, 2001
Date of Signature


Stephen A. Gratton